

**LAYERED PRESSURE SENSITIVE TRANSDUCER AND METHOD FOR MAKING SAME**

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**Abstract**

A variable resistance transducer responsive to an applied pressure is disclosed comprising a smooth resistive layer on a flexible substrate and deposited thereon by printing or imaging technology, small points of nonconductive or semiconductive material.

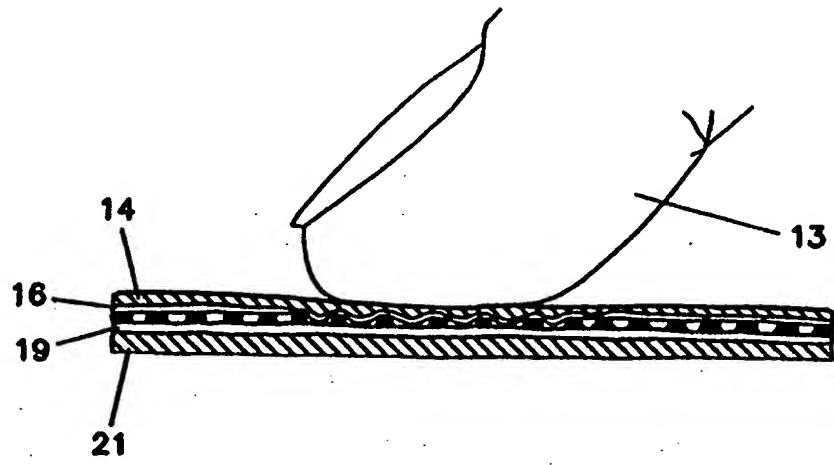
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<b>(21) International Application Number:</b> PCT/US95/01972 <b>(22) International Filing Date:</b> 16 February 1995 (16.02.95) <b>(30) Priority Data:</b> 08/198,149 17 February 1994 (17.02.94) US <b>(71) Applicant:</b> INTERLINK ELECTRONICS, INC. [US/US]; 546 Flynn Road, Camarillo, CA 93012 (US). <b>(72) Inventor:</b> YANIGER, Stuart, I.; 2402 Katherine Avenue, Ventura, CA 93003 (US). <b>(74) Agent:</b> BERMAN, Charles; Merchant, Gould, Smith, Edell, Welter & Schmidt, Suite 400, 11150 Santa Monica Boulevard, Los Angeles, CA 90025-3395 (US).		<b>(81) Designated States:</b> CA, JP, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).  <b>Published</b> <i>With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i>
<b>(54) Title:</b> LAYERED PRESSURE SENSITIVE TRANSDUCER AND METHOD FOR MAKING SAME    <b>(57) Abstract</b> <p>A variable resistance transducer responsive to an applied pressure is disclosed comprising a smooth resistive layer on a flexible substrate and deposited thereon by printing or imaging technology, small points of nonconductive or semiconductive material.</p>		